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A University of Oxford spinout startup aims to compete against US-based tech giants in providing access to quantum computing over the internet.

~~This quantum computer with a 3D chip is~~

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~~heading into the cloud~~

Researchers from Brown University and MIT have developed a new data science framework that allows users to process data with the programming language Python without paying the 'performance tax' ...

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~~New data science platform speeds up
Python queries~~

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Cheng Wang, co-founder and senior vice president of software and engineering at Flex Logix, sat down with Semiconductor Engineering to explain the process of bringing an inferencing accelerator chip ...

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~~Challenges In Developing A New
Inferencing Chip~~

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High-performance embedded computer
(HPEC) systems have begun to utilize the
specialized parallel computational speed
and performance on general-purpose
graphic processor units (GPGPUs),
enabling ...

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~~11 Myths About GPGPU Computing~~

The open software programming ... is built around the q16 processor architecture, which combines the software flexibility and programmability of a Turing complete parallel processor with the ...

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~~Quadric Reimagines General Purpose
Parallel Processing with an All New
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Architecture Optimized for On Device AI
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The cyber threat faced by the automotive industry reached public awareness in 2015, when a "White Hat" research team commandeered the control electronics of a target vehicle at freeway speeds.

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~~No Safety Without Dependable Security In
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Automotive Designs
B. Kirk~~

The global HPC chipset market size is expected to reach \$13.68 billion by 2027 from \$4.30 billion in 2019, growing at a CAGR of 19.1% from 2020 to 2027. High performance computing (HPC) has a ...

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~~High Performance Computing (HPC)
Chipset Market forecast to 2027: top
companies, trends & growth factors and
trend forecast to 2027~~

Given an unknown PCBA with an ARM processor, odds are good that it will ... into another (as in a PCI card) or hold them in

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parallel (as in a mini PCIe card or an m.2 SSD). The DebugEdge connector ...

~~This Debug Connector Brings Your Issues
To The Edge~~

Adoption of AMD EPYC processors in
Top500 list of world's fastest
supercomputers accelerates; number of

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AMD-powered systems doubles since
November and EPYC processors ...

~~AMD Leads High Performance~~

~~Computing Towards Exascale and Beyond~~

Like I've said before, I'm using the
Yamaha V9938 video display processor as
the graphics ... of EEPROM or Flash that

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can be accessed on a parallel bus. That means 15 address lines, 16 data ...

~~Hackaday 68k: So You Want A Kit?~~

Tilera had one of the first highly parallel SmartNIC ... in it has experience in P4 programming, and it wouldn't be a surprise for Nvidia to craft a P4 packet

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~~SmartNIC Architectures: A Shift to
Accelerators and Why FPGAs are Poised
to Dominate~~

The divides that once existed between
other disciplines have quickly become
foggy, whether between programming ...

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AI Edge can process sensor data in parallel
and apply AI inferences to action the Arm

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Kirk

~~Xilinx's Versal AI Edge Blends the
Border Between Programming and EEs~~
CUDA is a parallel computing platform
and programming model developed by

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Nvidia for general computing on graphical processing units. Lipacis and Nvidia chief financial officer Colette Kress ...

~~Nvidia Jumps On Jefferies Upgrade As
Shift In Computing Seen Helping~~

Data queries written in Python, a commonly used programming language ...

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tasks across multiple processor cores or machines in a data center. That parallel processing allows users to deal with ...

~~New data science platform speeds up
Python queries~~

PROVIDENCE, R.I. [Brown University]
-- Researchers from Brown University and

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MIT have developed a new data science framework that allows users to process data with the programming language Python ...

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